

1 42. The direct current sum bandgap voltage comparator of claim
2 31, further comprising:
3 a cascode stage connected to the summing node.

1 43. The direct current sum bandgap voltage comparator of claim
2 31, further comprising:
3 a clamping circuit connected to the summing node.

1 44. The direct current sum bandgap voltage comparator of claim
2 31, further comprising:
3 a plurality of inverters connected in series between the
4 summing node and an output node.

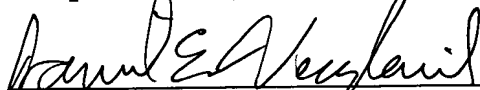
1 45. The direct current sum bandgap voltage comparator of claim
2 31, further comprising:
3 a hysteresis circuit connected to the output node and to the
4 fourth current source.--

REMARKS

Claims 27, 29, and 31-45 are pending in the present application. Claims 1, 4-14, 17-26, and 30 were canceled. Examination of the application is respectfully requested.

The Examiner is invited to call the undersigned at the below-listed telephone number if in the opinion of the Examiner such a telephone conference would expedite or aid the prosecution and examination of this application.

Respectfully submitted,



Dan Venglarik
Reg. No. 39,409
FELSMAN, BRADLEY, GUNTER
& DILLON, LLP
2600 Continental Plaza
777 Main Street
Fort Worth, TX 76102
(817) 332-8143
ATTORNEY FOR APPLICANTS